

September 2009

2SA733 PNP General Purpose Amplifier

Features

- This device is designed for general purpose amplifier applications at collector currents to 300 mA.
- · Sourced from Process 68.



Absolute Maximum Ratings* $T_A=25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-60	V
V _{CEO}	Collector-Emitter Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-5.0	V
I _C	Collector current - Continuous	-500	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics T_A=25°C unless otherwise noted

Symbol	Parameter	Max	Units
P _D	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C
$R_{ hetaJC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

¹⁾ These ratings are based on a maximum junction temperature of 150 degrees C.

²⁾ These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Electrical Characteristics T_A=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units			
Off Chara	Off Characteristics								
V _{CBO}	Collector-Base Breakdown Voltage	$I_C = -10\mu A, I_E = 0$	-60			V			
V _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = -1 \text{mA}, I_B = 0$	-50			V			
V _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -10\mu A, I_C = 0$	-5.0			V			
I _{CBO}	Collector Cut-off Current	V _{CB} = -60V, I _E = 0			-100	nA			
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			-100	nA			
On Characteristics									
h _{FE}	DC Current Gain	$V_{CE} = -6V$, $I_{C} = -1mA$	90		600				
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = -100 \text{mA}, I_B = -10 \text{mA}$	-15		-300	mV			
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -6V$, $I_{C} = -1mA$	-580		-680	mV			
Small Sig	Small Signal Characteristics								
f _T	Current Gain Bandwidth Product	$V_{CE} = -6V, I_{C} = -10mA$	50			MHz			
C _{ob}	Output Capacitance	$V_{CB} = -10V, I_{E} = 0$ f = 1.0MHz			6	pF			
NF	Noise Figure	$V_{CE} = -6V, I_{C} = -0.3\text{mA}$ $R_{G} = 10\text{k}\Omega, f = 100\text{Hz}$			20	dB			

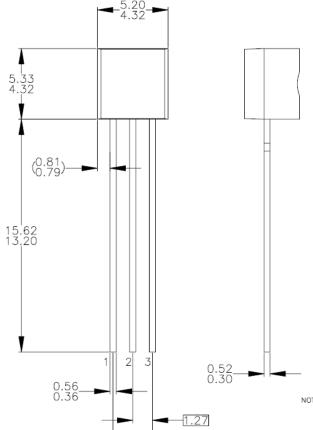
^{*} Pulse Test: Pulse Width $\leq 300~\mu\text{s},$ Duty Cycle $\leq 2.0\%$

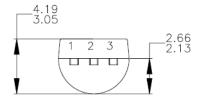
hFE Classification

Classification	R	Q	Р	К	
h _{FE}	90 ~ 180	135 ~ 270	200 ~ 400	300 ~ 600	

Physical Dimension

TO-92





2.54

NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 DRAWING CONFORMS TO ASME Y14.5M-1994.
 D) TO-92 (92,94,96,97,98) PIN CONFIGURATION:

Z Z		92			94			96			97			98	
<u>-</u>	Р	F	М	Р	F	М	Ρ	F	М	Р	F	М	Р	F	М
1	Ε	S	S	Ε	S	S	В	D	G	С	G	D	C	G	D
2	В	D	G	С	G	D	Ε	S	S	В	D	G	Ε	S	S
3	С	G	D	В	D	G	С	G	D	Ε	S	S	В	D	G

P - BIPOLAR F - JFET M - DMOS E - EMITTER B - BASE C - COLLECTOR D - DRAIN S - SOURCE G - GATE

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"
 ARE INTERCHANGEAGLE AT JFET "F" OPTION.
 F) DRAWING FILENAME: MKT-ZAO3DREV3.

Dimensions in Millimeters





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